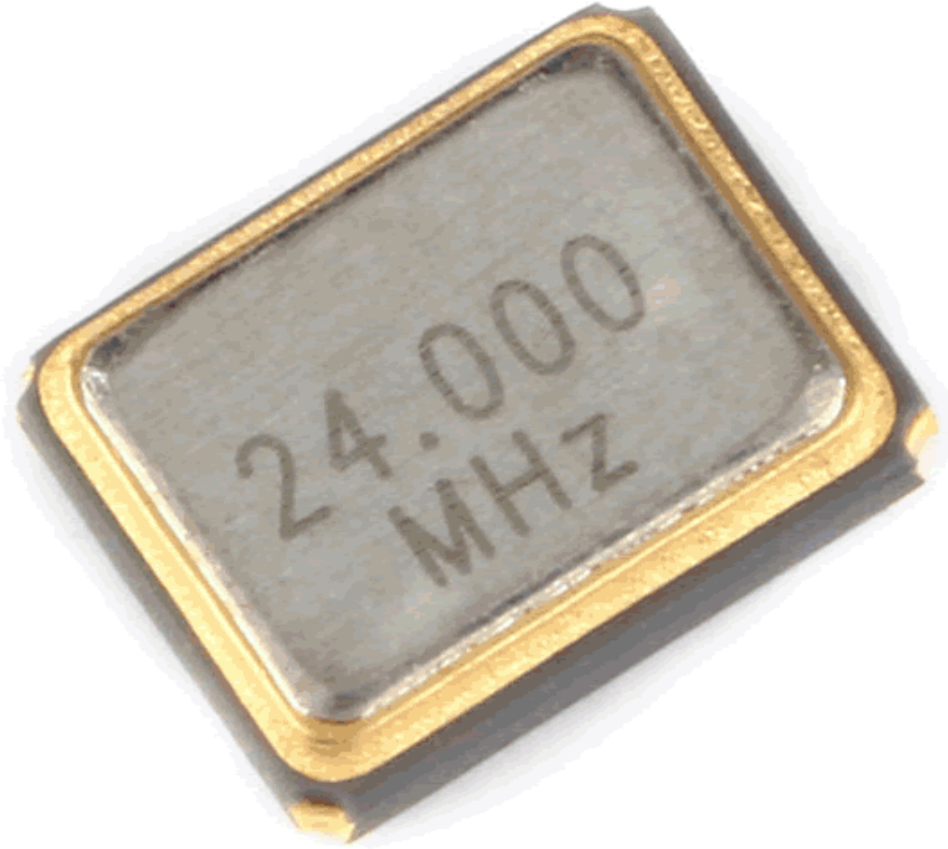


3225无源/24MHZ

3225 贴片无源晶振 24MHz $\pm 20\text{ppm}$ 20pF



1.This specification applies to quartz crystal unit with a frequency of 24.000MHz.

2.Electrical characteristics:

	Item	Conditions	Specifications
2.1	Holder type		SMD3225
2.2	Norminal frequency		24.000MHz
2.3	Adjustment tolerance	Reference temperature shall be $25\pm 2^{\circ}\text{C}$	$\pm 20\text{ppm}$
2.4	Frequency tolerance over temperature Range	Reference temperature shall be $25\pm 2^{\circ}\text{C}$	$\pm 20\text{ppm}$
2.5	Operating temperature range		$-20^{\circ}\text{C} \sim +75^{\circ}\text{C}$
2.6	Storage temperature range		$-40^{\circ}\text{C} \sim +85^{\circ}\text{C}$
2.7	Resonance resistance		$< 30\ \text{ohm}$
2.8	Load capacitance		20PF

2.9	DLD	0.01-100uW/20 steps	NA
2.10	RLD	0.01-100uW/20 steps	NA
2.11	Shunt capacitance	C0	7.0PF max
2.12	Motional capacitance	C1	NA
2.13	Pulling Sensitivity	S	NA
2.14	Drive level		100Uw typ
2.15	Insulation resistance	DC/100V shall be applied for 1min ,then measurement shall be made between terminal and case.	>500M ohm
2.16	Aging/year		<+/-2ppm

3.Measurement Circuit:

3.1 Frequency measurement:

Measurement circuit: 250B(Saunders)

Load capacitance:20PF Drive level : 100uW

3.2 Resistance measurement

Measurement circuit:250B(Saunders)

Load capacitance :Series Drive level : 100uW

4. Construction

4.1 Crystal enclosure seal:

Seam seal resistance weld cold weld

4.2 crystal enclosure medium

nitrogen vacuum dry air

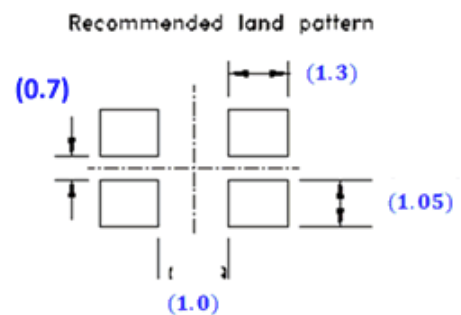
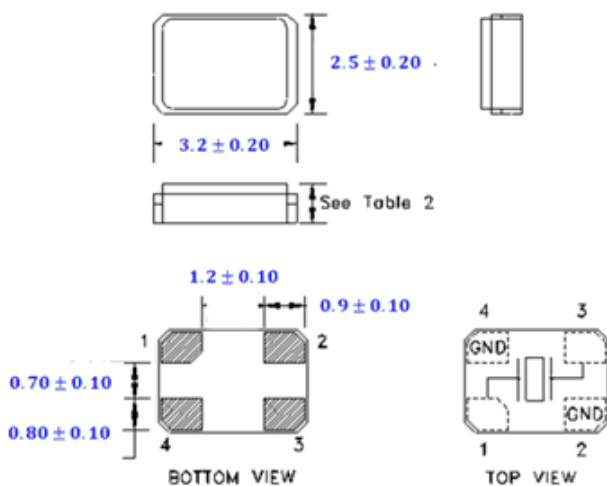


Table 2

Height (mm)	
ABM8	0.80 max
ABM81	0.65+/-0.05
ABM82	0.60+/-0.10

Note: Due to material availability, the chamfer could be located on pin #1, 3, or 4. Be advised that the chamfer location has no impact on the electrical performance of the device.

Dimensions: mm